

AMENDMENT TO THE SPECIFICATION

Please amend paragraph [0018] of the Specification as follows:

[0018] According to an embodiment of the invention, one or more of the PUs 104 are provided with a control port from which the PU can send information to the memory access unit 108 about the location of data to be read from or written to ~~an~~in external memory 120. The data and control ports are not explicitly labeled in Fig. 1, but are implied by virtue of the data lines 112 and the control lines 116, respectively. Addressing information and mode information, regarding a memory access channel, that is sent through a control port is destined to a memory access control register 127. The control register 127, for example, determines the settings for a DMA channel regarding the mode of operation of the DMA channel as well as location identifiers for the data that is to be transferred through the channel.